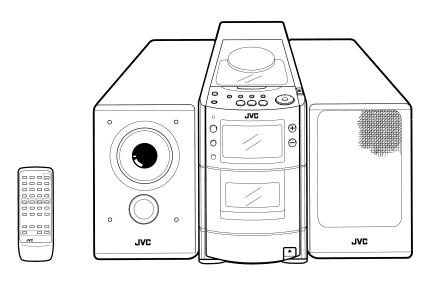
JVC

SERVICE MANUAL

MICRO COMPONENT SYSTEM

UX-T77





Area Suffix
UF China

Contents

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Safety precautions

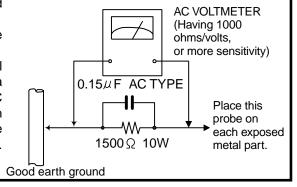
- 1. This design of this product contains special hardware and many circuits and components specially for safety purposes. For continued protection, no changes should be made to the original design unless authorized in writing by the manufacturer. Replacement parts must be identical to those used in the original circuits. Services should be performed by qualified personnel only.
- 2. Alterations of the design or circuitry of the product should not be made. Any design alterations of the product should not be made. Any design alterations or additions will void the manufacturer's warranty and will further relieve the manufacture of responsibility for personal injury or property damage resulting therefrom.
- 3. Many electrical and mechanical parts in the products have special safety-related characteristics. These characteristics are often not evident from visual inspection nor can the protection afforded by them necessarily be obtained by using replacement components rated for higher voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in the Parts List of Service Manual. Electrical components having such features are identified by shading on the schematics and by (1) on the Parts List in the Service Manual. The use of a substitute replacement which does not have the same safety characteristics as the recommended replacement parts shown in the Parts List of Service Manual may create shock, fire, or other hazards.
- 4. The leads in the products are routed and dressed with ties, clamps, tubings, barriers and the like to be separated from live parts, high temperature parts, moving parts and/or sharp edges for the prevention of electric shock and fire hazard. When service is required, the original lead routing and dress should be observed, and it should be confirmed that they have been returned to normal, after re-assembling.
- 5. Leakage current check (Electrical shock hazard testing) After re-assembling the product, always perform an isolation check on the exposed metal parts of the product (antenna terminals, knobs, metal cabinet, screw heads, headphone jack, control shafts, etc.) to be sure the product is safe to operate without danger of electrical shock. Do not use a line isolation transformer during this check.
 - Plug the AC line cord directly into the AC outlet. Using a "Leakage Current Tester", measure the leakage current from each exposed metal parts of the cabinet, particularly any exposed metal part having a return path to the chassis, to a known good earth ground. Any leakage current must not exceed 0.5mA AC (r.m.s.)
 - Alternate check method

Plug the AC line cord directly into the AC outlet. Use an AC voltmeter having, 1,000 ohms per volt or more sensitivity in the following manner. Connect a $1,500\Omega$ 10W resistor paralleled by

a 0.15μ F AC-type capacitor between an exposed metal part and a known good earth ground.

Measure the AC voltage across the resistor with the AC voltmeter.

Move the resistor connection to each exposed metal part, particularly any exposed metal part having a return path to the chassis, and measure the AC voltage across the resistor. Now reverse the plug in the AC outlet and repeat each measurement voltage measured any must not exceed 0.75 V AC (r.m.s.). This corresponds to 0.5 mA AC (r.m.s.).



Warning

- 1. This equipment has been designed and manufactured to meet international safety standards.
- 2. It is the legal responsibility of the repairer to ensure that these safety standards are maintained.
- 3. Repairs must be made in accordance with the relevant safety standards.
- 4. It is essential that safety critical components are replaced by approved parts.
- 5. If mains voltage selector is provided, check setting for local voltage.
- <u>AUTION</u> Burrs formed during molding may be left over on some parts of the chassis. Therefore, pay attention to such burrs in the case of preforming repair of this system.

Preventing static electricity

Electrostatic discharge (ESD), which occurs when static electricity stored in the body, fabric, etc. is discharged, can destroy the laser diode in the traverse unit (optical pickup). Take care to prevent this when performing repairs.

1.1. Grounding to prevent damage by static electricity

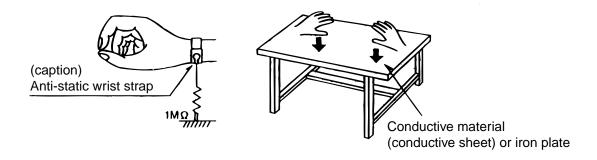
Static electricity in the work area can destroy the optical pickup (laser diode) in devices such as DVD players. Be careful to use proper grounding in the area where repairs are being performed.

1.1.1. Ground the workbench

1. Ground the workbench by laying conductive material (such as a conductive sheet) or an iron plate over it before placing the traverse unit (optical pickup) on it.

1.1.2. Ground yourself

1. Use an anti-static wrist strap to release any static electricity built up in your body.



1.1.3. Handling the optical pickup

- 1. In order to maintain quality during transport and before installation, both sides of the laser diode on the replacement optical pickup are shorted. After replacement, return the shorted parts to their original condition. (Refer to the text.)
- 2. Do not use a tester to check the condition of the laser diode in the optical pickup. The tester's internal power source can easily destroy the laser diode.

1.2. Handling the traverse unit (optical pickup)

- 1. Do not subject the traverse unit (optical pickup) to strong shocks, as it is a sensitive, complex unit.
- 2. Cut off the shorted part of the flexible cable using nippers, etc. after replacing the optical pickup. For specific details, refer to the replacement procedure in the text. Remove the anti-static pin when replacing the traverse unit. Be careful not to take too long a time when attaching it to the connector.
- 3. Handle the flexible cable carefully as it may break when subjected to strong force.
- 4. It is not possible to adjust the semi-fixed resistor that adjusts the laser power. Do not turn it

Important for laser products

1.CLASS 1 LASER PRODUCT

2.DANGER: Invisible laser radiation when open and inter lock failed or defeated. Avoid direct exposure to beam.

3.CAUTION: There are no serviceable parts inside the Laser Unit. Do not disassemble the Laser Unit. Replace the complete Laser Unit if it malfunctions.

4.CAUTION: The compact disc player uses invisible laserradiation and is equipped with safety switches whichprevent emission of radiation when the drawer is open and the safety interlocks have failed or are de feated. It is dangerous to defeat the safety switches.

5.CAUTION: If safety switches malfunction, the laser is able to function.

6.CAUTION: Use of controls, adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

♠ CAUTION Please use enough caution not to see the beam directly or touch it in case of an adjustment or operation check.

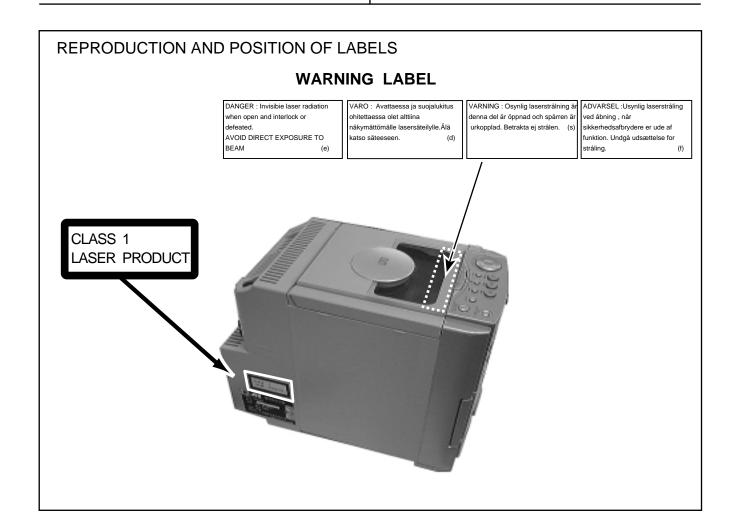
VARNING: Osynlig laserstrålning är denna del är öppnad och spårren är urkopplad. Betrakta ej strålen.

VARO : Avattaessa ja suojalukitus ohitettaessa olet

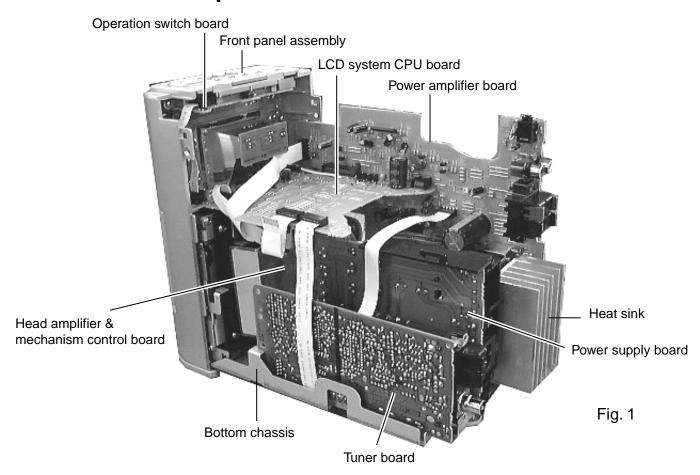
alttiina näkymättömälle lasersäteilylle.Älä katso säteeseen.

ADVARSEL: Usynlig laserstråling ved åbning, når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.

ADVARSEL: Usynlig laserstråling ved åpning,når sikkerhetsbryteren er avslott. unngå utsettelse for stråling.



Location of main parts



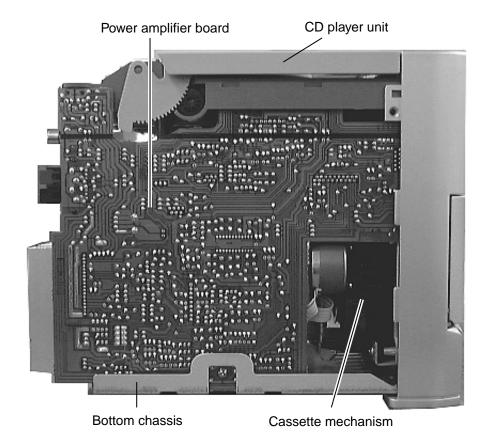
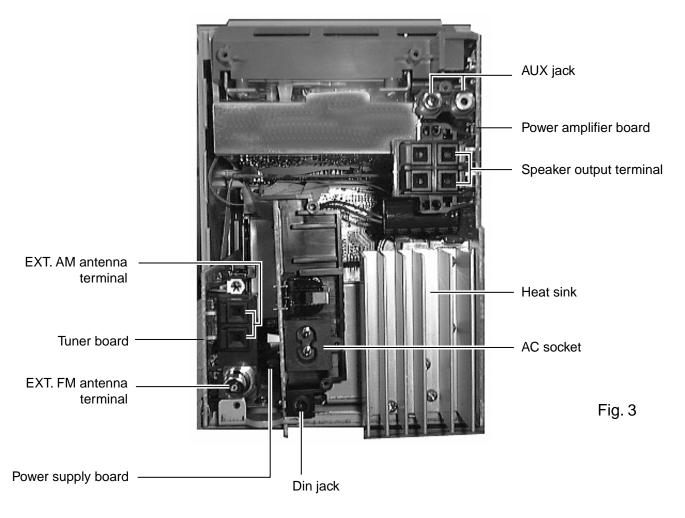
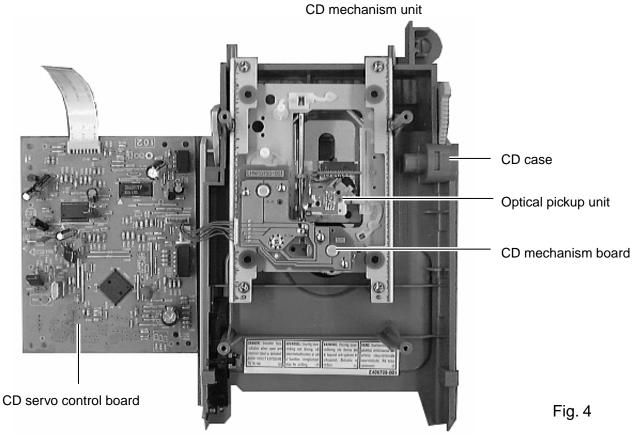


Fig. 2

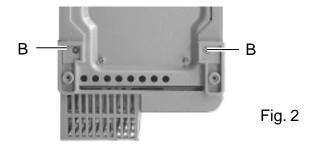




Disassembly method

■ Removing the rear panel (See Fig. 1 and 2)

- 1. From behind the body, remove the seven screws A retaining the rear panel.
- 2. After turning the body upside down, remove the two screws B retaining the rear panel.
- 3. Take out the rear panel from behind the body.

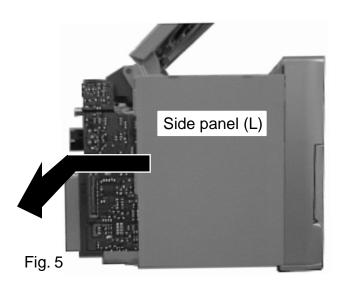


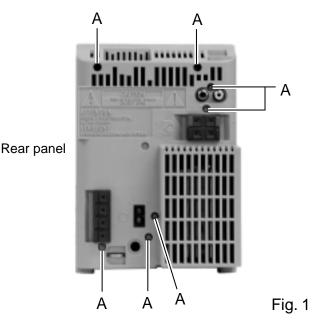
Rear panel

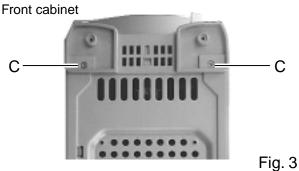
■ Removing the side panel (L and R)

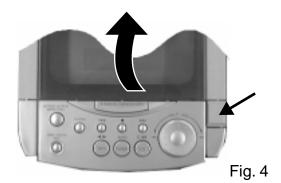
(See Fig. 3 to 6)

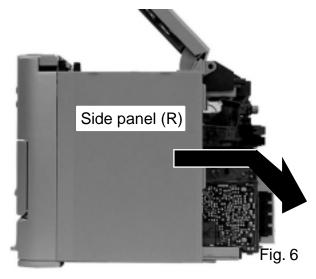
- 1. After turning the body upside down, remove the two screws C retaining the front cabinet assembly.
- 2. After turning the body back to initial position, open the CD door while pressing the upper OPEN button.
- 3. While moving the side panel (L) in the arrow direction, remove the panel from the left side of the body.
- 4. While moving the side panel (R) in the arrow direction, remove the panel from the right side of the body.











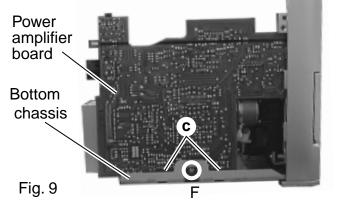
■ Removing the CD player unit

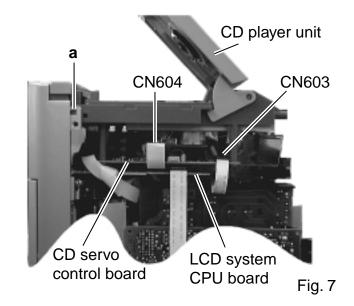
(See Fig. 7 and 8)

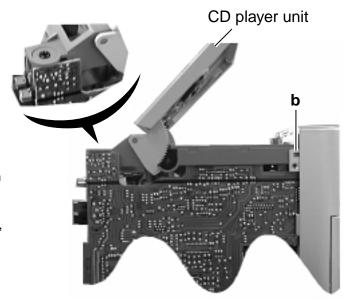
- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- From the connectors CN603,CN604 on the CD servo control board, disconnect the card wires outgoing from the connector CN304 on the power amplifier board and the connector CN766 on the LCD system CPU board.
- Disengage the left and right engagements a and b fixing the CD player unit by using a minus screw driver, etc.

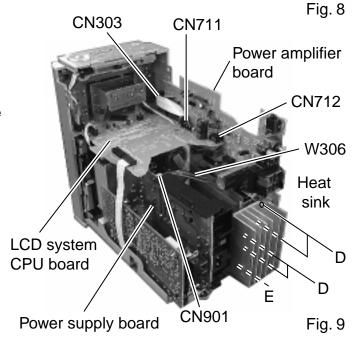
■ Removing the power amplifier board and heat sink (See Fig. 9 and 10)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- Remove the five screws D and E retaining the heat sink.
- From the connector CN303 on the power amplifier board, disconnect the card wire outgoing from the connector CN34 on the head amplifier and mechanism control board.
- 6. From the connector CN901 on the power supply board, disconnect the connector wire outgoing from the connector W306 on the power amplifier board.
- 7. Remove the one screw F retaining the power amplifier board.
- 8. Remove the power amplifier board from the two connectors CN711 and CN712 on the LCD system CPU board.
- 9. After removing the power amplifier board from the key way **c** on the bottom chassis, take this board out while raising it in the arrow direction.









■ Removing the tuner board (See Fig. 11)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. From the right side of the body, remove the one screw G retaining the tuner board.
- 5. From the connector CN1 on the tuner board, disconnect the card wire outgoing from the connector CN701 on the LCD system CPU board. (Fig.11a)

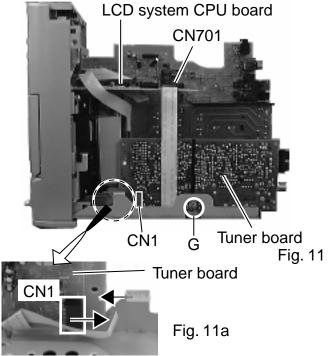
■ Removing the front panel assembly

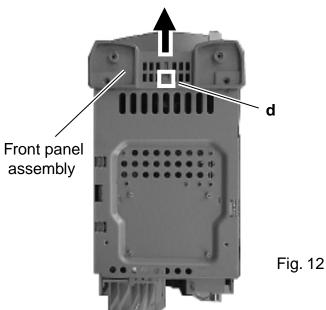
(See Fig. 12)

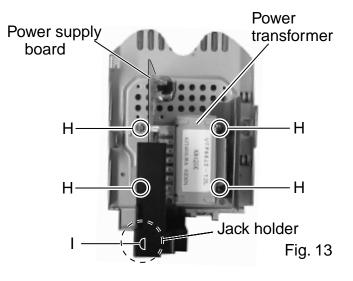
- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. Remove the power amplifier board and heat sink.
- 5. From the bottom of the body, disengage the engagement **d** fixing the front panel assembly in Fig. 12 while pressing in with a minus screw driver, eta.

■ Removing the power transformer and power supply board (See Fig. 13)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. Remove the power amplifier board and heat sink.
- 5. Remove the tuner board.
- Remove the four screws H retaining the power transformer and power supply board and the one screw I retaining the jack holder.







■ Removing the cassette mechanism unit

(See Fig. 14)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. Remove the power amplifier board.
- 5. Remove the front panel assembly.
- From inside the front panel assembly, remove the four screws J and K retaining the cassette mechanism unit.
- From the connector CN33 on the head amplifier and mechanism control board, disconnect the card wire outgoing from the connector CN731 on the LCD system CPU board.

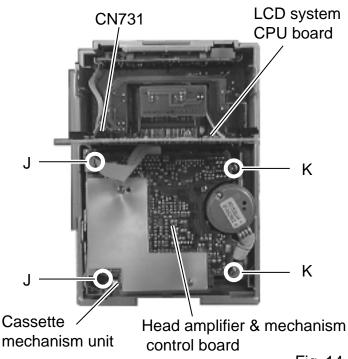


Fig. 14

■ Removing the function switch board and LCD system CPU board

(See Fig. 15 and 16)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. Remove the front panel assembly.
- 5. Remove the cassette mechanism unit.
- 6. From inside the front panel assembly remove the two screws L retaining the operating switch board.
- 7. From the connector CN782 on the LCD system CPU board, disconnect the connector wire outgoing from the connector CN802 on the operating switch board.
- 8. While sliding the two engagements **e** fixing the LCD system CPU board, pull out this board.
- After disconnect the connector CN801 on the function switch board from the connector CN781 on the LCD system CPU board, remove the respective boards while pulling them upward.

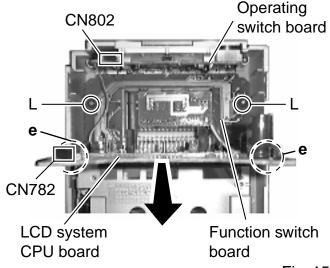
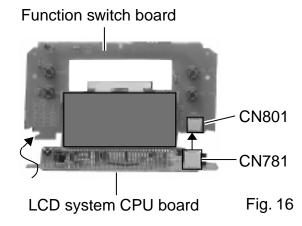


Fig. 15



■ Removing the operating switch board

(See Fig. 17 and 18)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- 4. Remove the front panel assembly.
- 5. Remove the cassette mechanism unit.
- 6. Remove the LCD system CPU board.
- 7. Remove the two screws M retaining the operating switch board.
- 8. Remove the [MULTI-JOG] button.
- 9. Remove the [VOLUME] and [POWER/AUX] buttons.
- 10. Take out the standby LED lens.
- 11. After removing four pawls **f** fixing the operating switch board, remove the switch while pulling it in the arrow direction.
- 12. Remove the two screws N retaining the operating swich board.
- 13. Remove the six pawls **g** fixing the operating switch board.

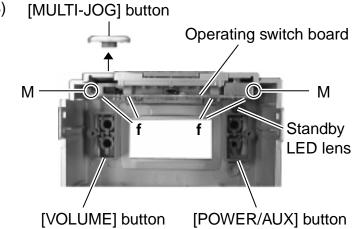
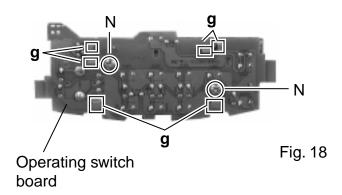


Fig. 17



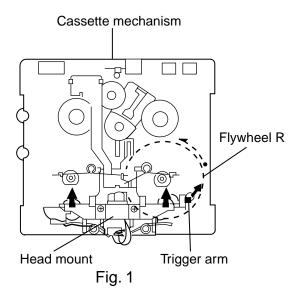
<<Cassette mechanism section>>

■ Removing the Playback/Recording & Eraser Head (See Fig. 1 to 3)

- 1. While shifting the trigger arms seen on the right side of the head mount in the arrow direction, turn the flywheel R in counterclockwise direction until the head mount has gone out with a click (See Fig. 1).
- 2. When the flywheel R is rotated in counterclockwise direction, the Playback/Recording & Eraser head will be turned in counterclockwise direction from the position in Fig. 2 to that in Fig. 3.
- At this position, disconnect the flexible P.C. board (outgoing from the Playback/Recording & Eraser head) from the connector CN31 on the head amplifier & mechanism control P.C. board.
- 4. After dismounting the FPC holder, remove the flexible P.C. board.
- 5. Remove the flexible P.C. board from the chassis base.
- 6. Remove the spring **1** from behind the Playback/Recording & Eraser head.
- 7. Loosen the reversing azimuth screw retaining the Playback /Recording & Eraser head.
- 8. Take out the Playback/Recording & Eraser head from the front of the head mount.
- 9. The Playback/Recoring & Eraser head should also be removed similarly according to Steps 1 to 8 above.

■ Reassembling the Playback/Recording & Eraser head (See Fig.3)

- 1. Reassemble the playback head from the front of the head mount to the position as shown in Fig. 3.
- 2. Fix the reversing azimuth screw.
- Set the spring 1 from behind the Playback/Recording & Eraser head.
- 4. Attach the flexible P.C. board to the chassis base, and fix it with the FPC holder as shown in Fig. 3.
- 5. The Playback/Recording & Eraser head should also be reassembled similarly to Step 1 to 4 above.



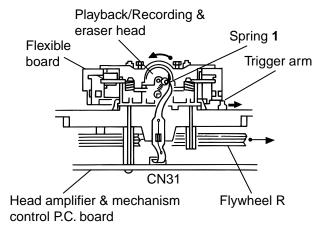
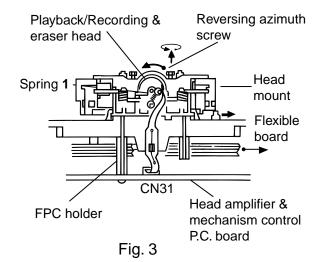


Fig. 2



■ Removing the Head amplifier & Mechanism control board (See Fig. 4)

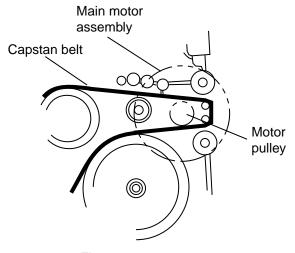
- 1. Remove the cassette mechanism assembly.
- 2. After turning over th cassette mechanism assembly, remove the three screws A retaining the head amplifier & mechanism control board.
- 3. Disconnect the connector CN32 on the board including the connector CN 1 on the reel pulse P.C. board.
- 4. When necessary, remove the 4 pin parallel wire soldered to the main motor.

Head amplifier & mechanism control board Fig. 4 Belt Main motor assembly B Wain parallel wire Main motor assembly

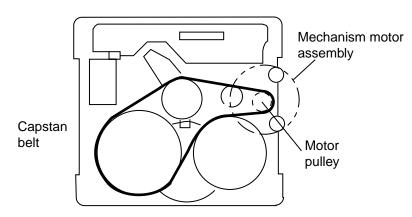
■ Removing the Main motor assembly

- 1. Remove the two screws B retaining the main motor assembly (See Fig. 4 and 4a).
- 2. While raising the main motor, remove the capstan belt from the motor pulley (See Fig. 4a).

Caution 1: Be sure to handle the capstan belt so carefully that this belt will not be stained by grease and other foreign matter. Moreover, this belt should be hanged while referring to the capstan belt hanging method in Fig. 5 and 6.









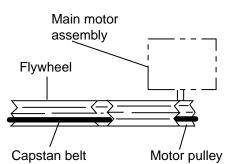
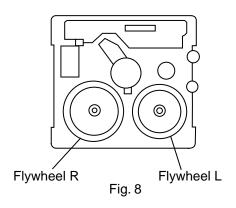
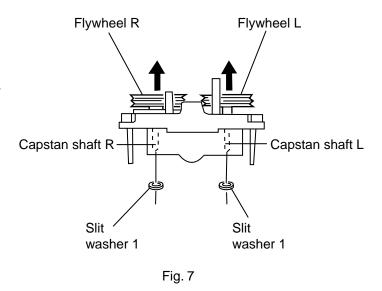


Fig. 6

■ Removing the Flywheel (See Fig. 7 and 8)

- 1. Remove the head amplifier & mechanism control P.C. board.
- 2. Remove the main motor assembly.
- After turning over the cassette mechanism, remove the slit washers 1 and fixing the capstan shafts R and L, and pull out the flywheel R and L respectively from behind the cassette mechanism.

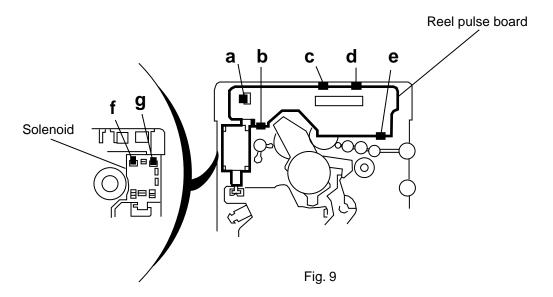




■ Removing the Reel Pulse P.C. board and Solenoid

(See Fig. 9)

- 1. Remove the five pails (a to reattaining the reel pulse board.
- 2. From the surface of the reel pulse board parts, remove the two pails lan retaining the solenoid.



<<CD Player Unit Section>>

Removing the CD mechanism and CD servo control P.C. board

(See Fig. 1 and 2)

- 1. Remove the rear panel.
- 2. Remove the left and right side panels.
- 3. Remove the CD player unit.
- After turning back the CD player unit, remove the four screws A retaining the CD servo control board and shield.
- From the connector CN601 on the CD servo control board, disconnect the card wire outgoing from the CD mechanism.
- 6. From the connector P011 on the CD mechanism P.C. board, disconnect the connector wire outgoing from the connector CN602 on the CD servo control board.
- 7. Remove the four screws B retaining the CD mechanism.
- 8. While sliding the CD mechanism in the arrow direction, take turn table out so carefully that it does not come into contact with the chassis.

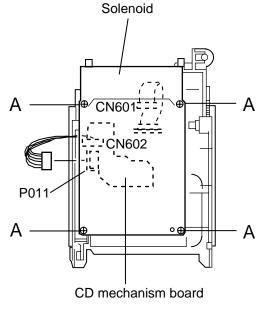
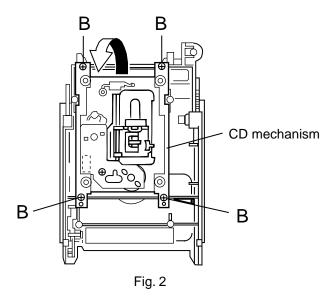


Fig. 1



Adjustment method

Measurement instruments required for adjustment

- 1. Low frequency oscillator This oscillator should have a capacity to output 0dBs to 600Ω at an oscillation frequency of 50Hz-20kHz.
- 2. Attenuator impedance: 600 Ω
- 3. Electronic voltmeter
- 4. Distortion meter
- 5. Frequency counter
- 6. Wow & flutter meter
- 7. Test tape

VT712: Tape speed and running unevenness (3kHz)

VT724 : Reference level (1kHz)

TMT7036: Head angle (10kHz), playback

frequency

characteristics (1kHz) and dubbing

frequency

characteristics (63, 1 and 10kHz)

Because of frequency -mixed tape with 63, 1, 10 and 14kHz (250nWb/m - 24dB), use this tape together with a filter.

8. Blank tape

TYPE I: AC-225

TYPE II: AC-514

9. Torque gauge: For play and back tension FWD(TW2111A), REV(TW2121a) and FF/REW(TW2231A)

■ Measurement conditions

Power supply voltage AC230V (50Hz)
Reference output Speaker : 0.775V/4 Ω
Headphone : 0.245V/32 Ω
Reference frequency and 1kHz, AUX: -8dBs
input level
Input for confirming recording and AUX: -28dBs
playback characteristics
Measurement output terminal Speaker J3002
$\mbox{\%}$ Load resistance 4Ω

Radio Input signal

AM frequency 400Hz
AM modulation 30%
FM frequency 400Hz
FM frequency deviation 22.5kHz

Tuner section

Voltage applied to tuner ----- +B: DC5.7V VT: DC 12V Reference measurement ----- 26.1mV(0.28V)/4 Ω Input positions ----- AM: Standard loop antenna FM: TP1 (hot) and TP2 (GND)

Standard measurement position of volume

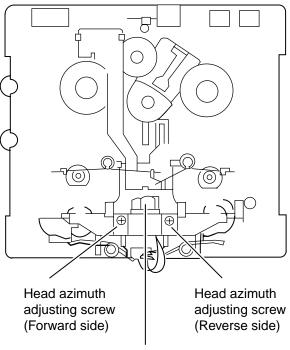
Bass Of	ff
Active hoper bass pro Of	f
Up and down adjustment of volume VOL: 23	3

Precautions for measurement

- 1. Apply 30pF and 33kΩ to the IF sweeper output side and $0.082\mu\,\mathrm{F}$ and $100\mathrm{k}\Omega$ in series to the sweeper input side.
- 2. The IF sweeper output level should be made as low as possible within the adjustable range.
- 3. Since the IF sweeper is a fixed device, there is no need to adjust this sweeper.
- 4. Since a ceramic oscillator is used, there is no need to perform any MIX adjustment.
- 5. Since a fixed coil is used, there is no need to adjust the FM tracking.
- 6. The input and output earth systems are separated. In case of simultaneously measuring the voltage in both of the input and output systems with an electronic voltmeter for two channels, therefore, the earth should be connected particularly carefully.
- 7. In the case of BTL connection amp., the minus terminal of speaker is not for earthing. Therefore, be sure not to connect any other earth terminal to this terminal. This system is of an BTL system.
- 8. For connecting a dummy resistor when measuring the output, use the wire with a greater code size.
- 9. Whenever any mixed tape is used, use the band pass filter (DV-12).

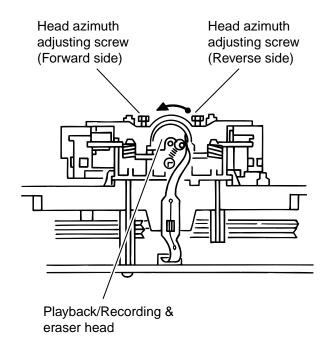
<< Arrangement of adjusting positions>>

Cassette mechanism section

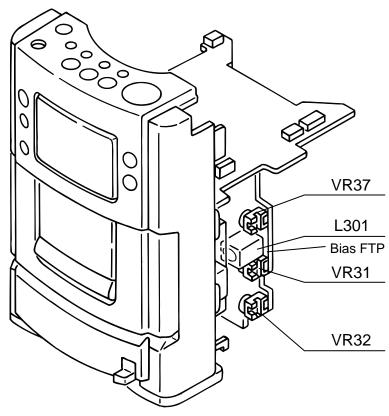


Playback/Recording & eraser head

Cassette mechanism section (Bottom side)



Front panel assembly section



■ Tape Recorder Section

Items	ns Measurement Measurement method		Standard Values	Adjusting positions	
Confirmation of head angle	: TMT7036 (10kHz) Measurement output terminal : Speaker terminal Speaker R	Playback the test tape TMT7036 (10kHz) With the recording & playback mechanism, adjust the head azimuth screw so that the forward and reverse output levels become maximum. After adjustment, lock the head azimuth at least by half turn. In either case, this adjustment should be performed in both the forward and reverse directions with the head azimuth screw.	Maximum output	Adjust the head azimuth screw only when the head has been changed.	
Confirmation of tape speed	Test tape : VT712 (3kHz) or TMT7036 (3kHz) Measurement output terminal : Headphone terminal	Adjust VR37 so that the frequency counter reading becomes 3,010Hz±15Hz when playing back the test tape VT712 (3kHz) with playback and recording mechanism after ending forward winding of the tape.	Tape speed of deck : 3,010Hz ±15Hz	VR37	

■ Reference Values for Confirmation Items

Items	Measurement conditions	Measurement method	Standard Values	Adjusting positions	
Difference between the forward and reverse speed	Test tape : VT712 (3kHz) or TMT7036 (3kHz) Measurement output terminal : Speaker terminal Speaker R (Load resistance: 4 \Omega) Measurement output terminal : Headphone	When the test tape VT712 (3kHz) has been played back with the recording and playback mechanism at the beginning of forward winding, the frequency counter reading of the difference between both of the mechanism should be 6.0Hz or less.	6.0Hz or less	Head azimuth screw (See Fig. 34)	
Wow & flutter	Test tape : VT712 (3kHz) or TMT7036 (3kHz) Measurement output terminal : Headphone terminal	When the test tape VT712 (3kHz) has been played back with the recording and playback mechanism at the beginning of forward winding, the frequency counter reading of wow & flutter should be 0.25% or less (WRMS).	0.25% or less (WRMS)		

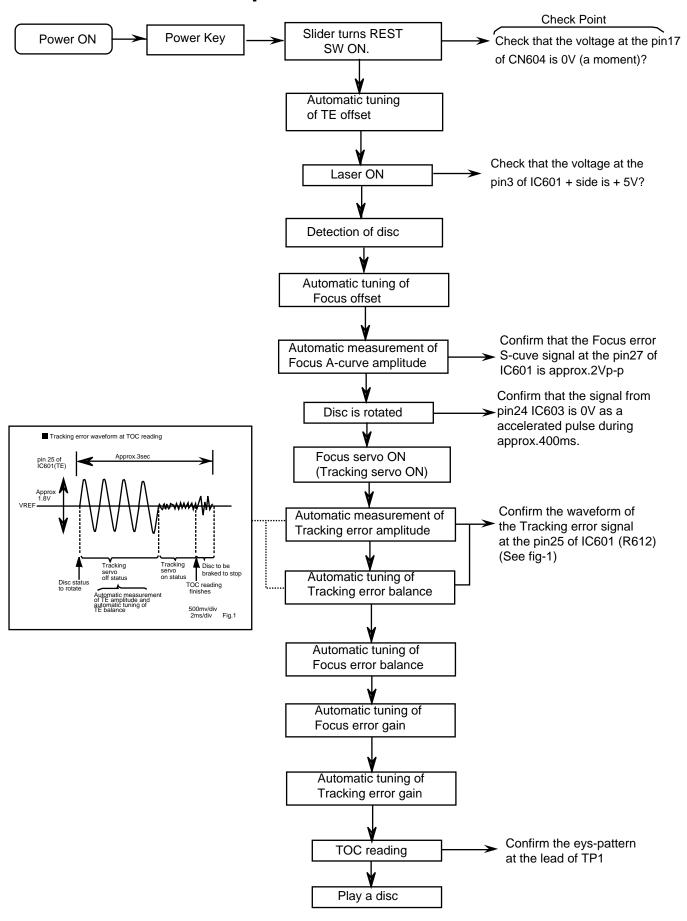
■ Electrical Performance

Items	Measurement conditions	Measurement method	Standard Values	Adjusting positions
Adjustment of recording bias current (Reference Value)	Mode: Forward or reverse mode Recording mode Test tape AC-514 to TYPE II and AC-225 to TYPE I Measurement output terminal Both recording and headphone terminals	 With the recording and playback mechanism, load the test tapes (AC-514 to TYPE II and AC-225 to TYPE I), and set the mechanism to the recording and pausing condition in advance. After connecting 100 Ω in series to the recorder head, measure the bias current with a valve voltmeter at both of the terminals. After resetting the [PAUSE] mode, start recording. At this time, adjust VR31 for Lch and VR32 for Rch so that the recording bias current values become 4.0 μ A (TYPE II). 	AC-225 : 4.20 μ A AC-514 : 4.0 μA	L ch : VR31 R ch : VR32
Adjustment of recording and playback frequency characteristics	Reference frequency : 1kHz and 10kHz (REF.: -20dB) Test tape : AC-514 to TYPE II Measurement input terminal : OSC IN	With the recording and playback mechanism, load the test tapes (AC-514 to TYPE II), and set the mechanism to the recording and pausing condition in advance. While repetitively inputting the reference frequency signal of 1kHz and 10kHz from OSC IN, record and playback the rape. While recording and playback the test tape in TYPE II, adjust VR31 for Lch and VR32 for Rch so that the output deviation between 1kHz and 10kHz becomes -1dB ±2dB.	Output deviation between 1kHz and 10kHz : -1dB ± 2dB	L ch :VR31 R ch :VR32

■ Reference Values for Electrical Function Confirmation Items

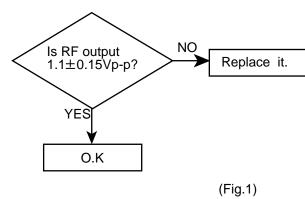
Items	Measurement conditions	Measurement method	Standard Values	Adjusting positions
Recording bias frequency	Forward or reverse Test tape :TYPE II (AC-514) Measurement terminal: BIAS TP on P.C. board	 While changing over to and from BIAS 1 and 2, confirm that the frequency is changed. With the recording and playback mechanism, load the test tape. (AC-514 to TYPE II), and set the mechanism to the recording and pausing condition in advance. Confirm that the BIAS TP frequency on the P.C. board is 100kHz ± 6kHz. 	100 kHz ± 6 kHz	
Eraser current (Reference value)	Forward or reverse Recording mode Test tape AC-514 to TYPE II and AC-225 to TYPE I Measurement terminal: Both of the eraser head terminals	1 While recording and playback mechanism, load the test tapes (AC-514 to TYPE II and AC-225 to TYPE I), and set the mechanism to the recording and pausing conditions in advance. 2 After setting to the recording conditions, connect 1W in series to the eraser head on the recording and playback mechanism side, and measure the eraser current from both of the eraser terminals.	TYPE II : 120 mA TYPE I : 75 mA	

Flow of functional operation until TOC read



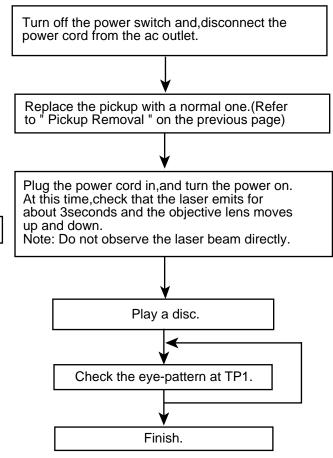
Maintenance of laser pickup

- Cleaning the pick up lens
 Befor you replace the pick up, please try to
 clean the lens with a alcohol soaked cotton
 swab.
- (2) Life of the laser diode (Fig.1)
 When the life of the laser diode has expired, the following symptoms wil appear.
 - (1) The level of RF output (EFM output:amplitude of eye pattern) will below.



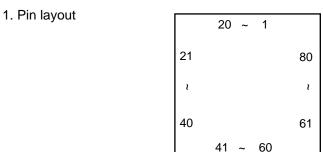
(3) Semi-fixed resistor on the APC PC board
The semi-fixed resistor on the APC printed
circuit board which is attached to the pickup
is used to adjust the laser power. Since this
adjustment should be performed to match the
characteristics of the whole optical block,
do not touch the semi-fixed resistor.
If the laser power is lower than the specified
value, the laser diode is almost worn out, and
the laser pickup should be replaced.
If the semi-fixed resistor is adjusted while
the pickup is functioning normally, the laser
pickup may be damaged due to excessive current.

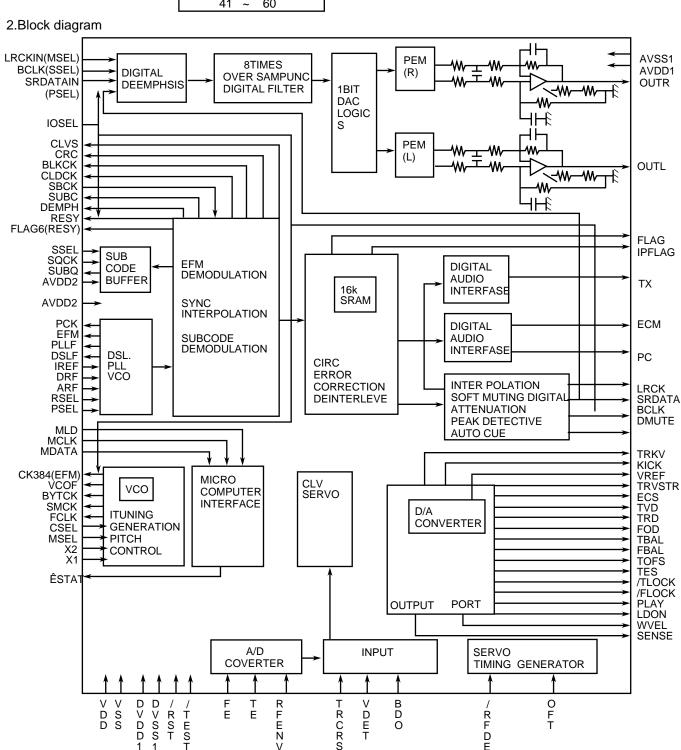
Replacement of laser pickup



Description of major ICs

■MN35510 (IC603) : Digital servo & digital signal processor





3. Pin function

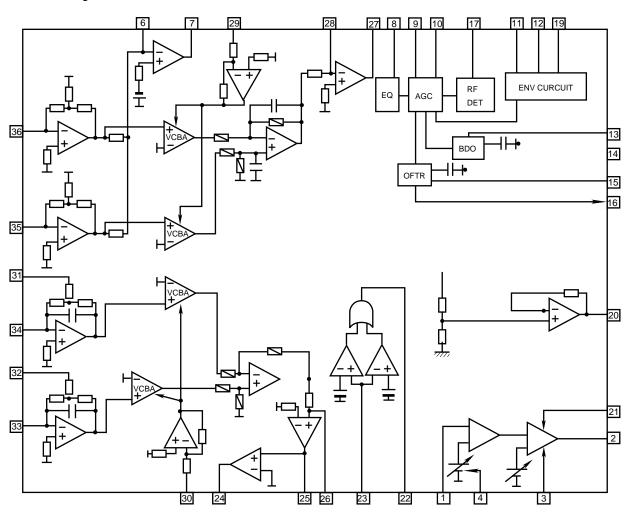
	Pin	symbol	1/0	Function	Pin	symbol	I/O	Function
2	No.	-,			No.	,		
SRDATA O Not used 43 WVEL - Not used 44 ARF I Resignal input			_				0	0 1 1 ,
A	—		_					
5 DVSS1 - Connected to GND			0					
6 TX 0 Digital audio interface output 46 DRF I Bias pin for DSL 7 MCLK I Zoom command clock signal input (Data is latched at signal's rising point) 47 DSLF VO Loop filter pin for DSL 8 MDATA I zoom command data input 48 PLLF IVO Loop filter pin for PLL 9 MLD I zoom command data input 49 VCOF Not used 10 SENSE O Not used 50 AVDD2 Power supply(Analog) 11 FLOCK O Not used 51 AVSS2 Connected to GND(Analog) 12 TLOCK O Not used 52 EFM Not used 13 BLKCK O Not used 52 EFM Not used 14 SOCK I Outside clock for sub-code Oresister input 54 PDO Not used 15 SUBO O Subcode Not used Not used 16 DMTE </td <td>—</td> <td></td> <td>_</td> <td></td> <td></td> <td></td> <td></td> <td></td>	—		_					
Mount Mou	-		$\overline{}$		_			' '
MOLK	6	1X		·	46	DRF	ı	Bias pin for DSL
9 MLD	7	MCLK	I	•	47	DSLF	I/O	Loop filter pin for DSL
10	8		I	μ com command data input	48		I/O	Loop filter pin for PLL
11 FLOCK O Not used 51 AVSS2 Connected to GND(Analog)	9		I	μ com command load signal input	49	VCOF	_	Not used
12 TLOCK O Not used 52 EFM - Not used 53 RCK - Not used 54 Reference voltage input 53 RCK - Not used 55 RCK - Not used 75 RCK - RCK	10	SENSE	0	Not used	50		_	Power supply(Analog)
13 BLKCK 0 Sub-code block clock signal output 53 PCK - Not used	11	FLOCK	0	Not used	51	AVSS2	_	Connected to GND(Analog)
14 SQCK	12	TLOCK	0	Not used	52	EFM	_	Not used
15 SUBQ O Sub-code Q -code output 55 SUBC — Not used	13	BLKCK	0	sub-code · block · clock signal output	53	PCK	_	Not used
Texas	14	SQCK	Ι	Outside clock for sub-code Q resister input	54	PDO	_	Not used
STATUS STATUS STATUS STATUS STATUS CRC, CUE, CLVS, TTSTOP, ECLV, SQOK) STATUS CRC, CUE, CLVS, TTSTOP, ECLV, SQOK) STATUS CRC, CUE, CLVS, TTSTOP, ECLV, SQOK) STATUS STATUS CRC, CUE, CLVS, TTSTOP, ECLV, SQOK) STATUS STATUS CRC, CUE, CLVS, TTSTOP, ECLV, SQOK) STATUS	15	SUBQ	0	Sub-code Q -code output	55	SUBC	_	Not used
SATUS O CRC.CUE,CLVS,TTSTOP,ECLV,SQOK) S7 VSS - circuit)	16	DMUTE	_	Connected to GND	56	SBCK	_	Not used
19 SMCK - Not used 59 X2 O Output of Xtal oscillation circuit 20 PMCK - Not used 60 VDD - Power supply(for Xtal oscillation circuit) 21 TRV O Traverse enforced output 61 BYTCK - Not used 22 TVD O Traverse drive output 62 CLDCK - Not used 23 PC - Not used 63 FCLK - Not used 24 ECM O Spindle motor drive signal (Enforced mode output) 3-State 64 IPPLAG - Not used 25 ECS O Spindle motor drive signal (Servo error signal (Servo error signal upput) 65 FLAG - Not used 26 KICK O Kick pulse output 66 CLVS - Not used 27 TRD O Tracking drive output 67 CRC - Not used 28 FOD O Focus drive output pin for D/A output block (TVD,FOD,FBA,TBAL) 69 RESY - Not used 30 FBAL O Focus Balance adjust signal output 70 IOSEL - pull up	17	STATUS	0	S .	57	VSS	_	,
20 PMCK - Not used 60 VDD - Power supply(for X'tal oscillation circuit) 21 TRV 0 Traverse enforced output 61 BYTCK - Not used 22 TVD 0 Traverse drive output 62 CLDCK - Not used 23 PC - Not used 63 FCLK - Not used 24 ECM 0 Spindle motor drive signal (Enforced mode output) 3-State 64 IPPLAG - Not used 25 ECS 0 Spindle motor drive signal (Servo error signal output) 65 FLAG - Not used 26 KICK 0 Kick pulse output 66 CLVS - Not used 27 TRD 0 Tracking drive output 67 CRC - Not used 28 FOD 0 Focus drive output 68 DEMPH Not used 29 VREF 1 Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 69 RESY - Not used 30 FBAL 0 Focus Balance adjust signal output 70 IOSEL - pull up 31 TBAL 0 Tracking Balance adjust signal output 71 TEST - pull up 32 FE 1 Focus error signal input(Analog input) 72 AVDD1 - Power supply(Digital) 33 TE 1 Tracking error signal input(Analog input) 74 AVSS1 - Connected to GND 35 VDET 1 Vibration detect signal input(H:detect) 75 OUT R 0 Rch audio output 36 RFDET 1 RF detect signal input(L:detect) 78 PSEL - Connected to GND 38 RFDET 1 RF detect signal input(L:detect) 79 MSEL - Connected to GND	18	RST	Ι	Reset signal input (L:Reset)	58	XI	_	Input of 16.9344MHz X'tal oscillation circuit
21 TRV O Traverse enforced output 61 BYTCK - Not used 22 TVD O Traverse drive output 62 CLDCK - Not used 23 PC - Not used 63 FCLK - Not used 24 ECM O Spindle motor drive signal (Enforced mode output) 3-State 25 ECS O Spindle motor drive signal (Servo error signal output) 26 KICK O Kick pulse output 66 CLVS - Not used 27 TRD O Tracking drive output 67 CRC - Not used 28 FOD O Focus drive output 68 DEMPH Not used 29 VREF I Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 30 FBAL O Focus Balance adjust signal output 70 IOSEL - pull up 31 TBAL O Tracking Balance adjust signal output 71 TEST - pull up 32 FE I Focus error signal input(Analog input) 72 AVDD1 - Power supply(Digital) 33 TE I Tracking error signal input(Analog input) 73 OUT L O Lch audio output 34 RF ENV I RF envelope signal input(H:detect) 75 OUT R O Rch audio output 37 TRCRS I Track cross signal input (H:detect) 76 RSEL - pull up 37 TRCRS I Track cross signal input (H:detect) 78 PSEL - Connected to GND 38 RFDET I RF detect signal input(L:detect) 79 MSEL - Connected to GND 39 BDO I BDO input pin(L:detect) 79 MSEL - Connected to GND	19	SMCK	-	Not used	59	X2	0	Output of X'tal oscillation circuit
22 TVD O Traverse drive output 62 CLDCK — Not used 23 PC — Not used 63 FCLK — Not used 24 ECM O Spindle motor drive signal (Enforced mode output) 3-State 64 IPPLAG — Not used 25 ECS O Spindle motor drive signal (Servo error signal output) 65 FLAG — Not used 26 KICK O Kick pulse output 66 CLVS — Not used 27 TRD O Tracking drive output 67 CRC — Not used 28 FOD O Focus drive output 68 DEMPH Not used 29 VREF I Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 69 RESY — Not used 30 FBAL O Focus Balance adjust signal output 70 IOSEL — pull up 31 TBAL O Tracking Balance adjust signal output 71 TEST — pull up 32	20	PMCK	-	Not used	60	VDD	-	Power supply(for X'tal oscillation circuit)
PC - Not used 63 FCLK - Not used ECM O Spindle motor drive signal (Enforced mode output) 3-State 64 IPPLAG - Not used ECS O Spindle motor drive signal (Servo error signal output) 65 FLAG - Not used ECS O Spindle motor drive signal (Servo error signal output) 66 CLVS - Not used ECS KICK O Kick pulse output 67 CRC - Not used TRD O Tracking drive output 68 DEMPH Not used VREF I Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 69 RESY - Not used VREF I Reference adjust signal output 70 IOSEL - pull up TBAL O Tracking Balance adjust signal output 71 TEST - pull up TEST - pull up TEST - Power supply(Digital) TEST - Power supply(Digital) TEST - Connected to GND FRENV I RE envelope signal input(Analog input) 74 AVSS1 - Connected to GND TRCRS I Track cross signal input (L:detect) 75 OUT R O Rch audio output TRCRS I Track cross signal input (L:detect) 78 PSEL - Connected to GND BDO II BDO input pin(L:detect) 79 MSEL - Connected to GND	21	TRV	0	Traverse enforced output	61	BYTCK		Not used
ECM O Spindle motor drive signal (Enforced mode output) 3-State ECS O Spindle motor drive signal (Servo error signal output) Spindle motor drive signal input pin or Drive output sed. Spindle motor drive signal input pin or Drive output sed. Spindle motor drive signal input pin or Drive output sed. Spindle motor drive signal input pin or Drive output sed. Spindle motor drive signal input output of CRC or Not used Spindle motor drive signal input pin or Drive output ou	22	TVD	0	Traverse drive output	62	CLDCK	-	Not used
ECM O mode output) 3-State 64 IPPLAG — Not used	23	PC	_	Not used	63	FCLK	_	Not used
26 KICK O Kick pulse output 66 CLVS — Not used 27 TRD O Tracking drive output 67 CRC — Not used 28 FOD O Focus drive output 68 DEMPH Not used 29 VREF I Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 30 FBAL O Focus Balance adjust signal output 70 IOSEL — pull up 31 TBAL O Tracking Balance adjust signal output 71 TEST — pull up 32 FE I Focus error signal input(Analog input) 72 AVDD1 — Power supply(Digital) 33 TE I Tracking error signal input(Analog input) 73 OUT L O Lch audio output 34 RF ENV I RF envelope signal input(Analog input) 74 AVSS1 — Connected to GND 35 VDET I Vibration detect signal input (H:detect) 75 OUT R O Rch audio output 36 OFT I Off track signal input (H:off track) 76 RSEL — pull up 37 TRCRS I Track cross signal input (L:detect) 78 PSEL — Connected to GND 38 RFDET I RF detect signal input(L:detect) 79 MSEL — Connected to GND	24	ECM	0		64	IPPLAG	-	Not used
27TRDOTracking drive output67CRC—Not used28FODOFocus drive output68DEMPHNot used29VREFIReference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL)69RESY—Not used30FBALOFocus Balance adjust signal output70IOSEL—pull up31TBALOTracking Balance adjust signal output71TEST—pull up32FEIFocus error signal input(Analog input)72AVDD1—Power supply(Digital)33TEITracking error signal input(Analog input)73OUT LOLch audio output34RF ENVIRF envelope signal input(Analog input)74AVSS1—Connected to GND35VDETIVibration detect signal input(H:detect)75OUT RORch audio output36OFTIOff track signal input(H:off track)76RSEL—pull up37TRCRSITrack cross signal input77CSEL—Connected to GND38RFDETIRF detect signal input(L:detect)78PSEL—Connected to GND39BDOIBDO input pin(L:detect)79MSEL—Connected to GND	25	ECS	0		65	FLAG	-	Not used
FOD O Focus drive output Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) FBAL O Focus Balance adjust signal output TO IOSEL — pull up TEST — pull up Tracking Balance adjust signal output TO IOSEL — pull up TO IOSEL — POWER SUPPLY (Digital) TO IOSEL — Connected to GND TO IOSEL — POWER SUPPLY (Digital) TO IOSEL — POWER SUPPLY (DIGITAL SUPP	26	KICK	0	Kick pulse output	66	CLVS	_	Not used
VREF I Reference voltage input pin for D/A output block (TVD,FOD,FBA,TBAL) 69 RESY — Not used	27	TRD	0	Tracking drive output	67	CRC	_	Not used
Output block (TVD,FOD,FBA,TBAL) O Focus Balance adjust signal output To IOSEL — pull up Tracking Balance adjust signal output To ITEST — pull up Tracking Balance adjust signal output To ITEST — pull up Tracking Balance adjust signal output To ITEST — pull up To ITEST — pull up	28	FOD	0	Focus drive output	68	DEMPH		Not used
31TBALOTracking Balance adjust signal output71TEST— pull up32FEIFocus error signal input(Analog input)72AVDD1— Power supply(Digital)33TEITracking error signal input(Analog input)73OUT LOLch audio output34RF ENVIRF envelope signal input(Analog input)74AVSS1— Connected to GND35VDETIVibration detect signal input(H:detect)75OUT RORch audio output36OFTIOff track signal input(H:off track)76RSEL— pull up37TRCRSITrack cross signal input77CSEL— Connected to GND38RFDETIRF detect signal input(L:detect)78PSEL— Connected to GND39BDOIBDO input pin(L:detect)79MSEL— Connected to GND	29	VREF	ı		69	RESY	_	Not used
32 FE I Focus error signal input(Analog input) 72 AVDD1 - Power supply(Digital) 33 TE I Tracking error signal input(Analog input) 73 OUT L O Lch audio output 34 RF ENV I RF envelope signal input(Analog input) 74 AVSS1 - Connected to GND 35 VDET I Vibration detect signal input(H:detect) 75 OUT R O Rch audio output 36 OFT I Off track signal input(H:off track) 76 RSEL - pull up 37 TRCRS I Track cross signal input 77 CSEL - Connected to GND 38 RFDET I RF detect signal input(L:detect) 78 PSEL - Connected to GND 39 BDO I BDO input pin(L:detect) 79 MSEL - Connected to GND	30	FBAL	0	Focus Balance adjust signal output	70	IOSEL	-	pull up
33 TE	31	TBAL	0	Tracking Balance adjust signal output	71	TEST	_	pull up
34 RF ENV I RF envelope signal input(Analog input) 74 AVSS1 — Connected to GND 35 VDET I Vibration detect signal input(H:detect) 75 OUT R O Rch audio output 36 OFT I Off track signal input(H:off track) 76 RSEL — pull up 37 TRCRS I Track cross signal input 77 CSEL — Connected to GND 38 RFDET I RF detect signal input(L:detect) 78 PSEL — Connected to GND 39 BDO I BDO input pin(L:detect) 79 MSEL — Connected to GND	32	FE	I	Focus error signal input(Analog input)	72	AVDD1	_	Power supply(Digital)
35 VDET	33	TE	Τ	Tracking error signal input(Analog input)	73	OUT L	0	Lch audio output
36 OFT I Off track signal input(H:off track) 76 RSEL - pull up 77 CSEL - Connected to GND 78 RFDET I RF detect signal input(L:detect) 78 PSEL - Connected to GND 79 MSEL - Connected to GND	34	RF ENV	Ι	RF envelope signal input(Analog input)	74	AVSS1		Connected to GND
37 TRCRS I Track cross signal input 77 CSEL - Connected to GND 38 RFDET I RF detect signal input(L:detect) 78 PSEL - Connected to GND 39 BDO I BDO input pin(L:detect) 79 MSEL - Connected to GND	35	VDET	I	Vibration detect signal input(H:detect)	75	OUT R	0	Rch audio output
38 RFDET I RF detect signal input(L:detect) 39 BDO I BDO input pin(L:detect) 78 PSEL — Connected to GND 79 MSEL — Connected to GND	36	OFT	ı	Off track signal input(H:off track)	76	RSEL	-	pull up
39 BDO I BDO input pin(L:detect) 79 MSEL - Connected to GND	37	TRCRS	ı	Track cross signal input	77	CSEL	_	Connected to GND
39 BDO I BDO input pin(L:detect) 79 MSEL - Connected to GND	38	RFDET	ı	RF detect signal input(L:detect)	78	PSEL	_	Connected to GND
	39	BDO	Т	BDO input pin(L:detect)	79	MSEL	_	Connected to GND
- - - - - - - - - -	40	LDON	0	Laser ON signal output(H:on)	80	SSEL	-	Pull up

■ AN8806SB-W (IC601) : RF & Servo AMP

1.Pin layout

PD	1	\cup	36	PDAC
LD	2		35	PDBD
LDON	3		34	PDF
LDP	4		33	PDE
VCC	5		32	PDER
RF-	6		31	PDFR
RF OUT	7		30	TBAL
RF IN	8		29	FBAL
C.AGC	9		28	EF-
ARF	10		27	EF OUT
C.ENV	11		26	TE-
C.EA	12		25	TE OUT
CS BDO	13		24	CROSS
BDO	14		23	TE BPF
CS BRT	15		22	VDET
OFTR	16		21	LD OFF
/NRFDET	17		20	VREF
GND	18		19	ENV

2.Block diagram

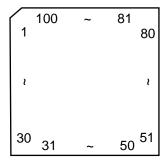


3. Pin function

Pin No.	Symbol	I/O	Function
1	PD	I	APC amp input terminal
2	LD	0	APC amp output terminal
3	LD ON	I	APC ON/OFF control terminal
4	LDP		Connect to ground
5	VCC		Power supply
6	RF-	Ι	Inverse input pin for RF amp
7	RF OUT	0	RFamp output
8	RF IN	I	RF input
9	C.AGC	I/O	Connecting pin of AGC loop filter
10	ARF	0	RF output
11	C.ENV	I/O	A capacitor is connected to this terminal to detect the envelope of RF signal
12	C.EA	I/O	A capacitor is connected to this terminal to detect the envelope of RF signal
13	CS BDO	I/O	A capacitor is connected to detect the lower envelope of RF signal
14	BDO	0	BDO output pin
15	CS BRT	I/O	A capacitor is connected to detect the lower envelope of RF signal
16	OFTR	0	Of-track status signal output
17	/NRFDET	0	RF detection signal output
18	GND		Ground
19	ENV	0	Envelope output
20	VREF	0	Reference voltage output
21	LD OFF		Connect to ground
22	VDET	0	Vibration detection signal output
23	TE BPF	-	Input pin of tracking error through BPF
24	CROSS	0	Tracking error cross output
25	TE OUT	0	Tracking error signal output
26	TE-	I	Inverse input pin for tracking error amp
27	FE OUT	0	Output pin of focus error
28	FE-	I	Inverse input pin for focus error amp
29	FBAL	ı	Focus balance control
30	TBAL	I	Tracking balance control
31	PDFR	I/O	F I-V amp gain control
32	PDER	I/O	E I-V amp gain control
33	PDF	Ι	I-V amp input
34	PDE	I	I-V amp input
35	PD BD	I	I-V amp input
36	PD AC	Ι	I-V amp input

■ UPD78064GF-108 (IC701) : System CPU

1.Pin layout

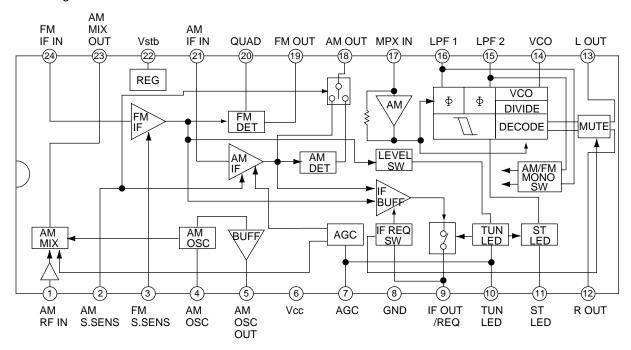


2.Pin function

Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	USDATA	I/O	Serial data	29	URESET/CLOSE	ı	[RESET/CLOSE] switch
2	USCK	0	Serial clock	30	USAFETY1	ı	Abnormal voltage detection 1
3	UQRIN	Т	CD Q code data	31	USAFETY0	ı	Abnormal voltage detection 2
4	UNC	-	Non connect	32	UKEY1		Body key input 1
5	USQCK	0	CD Q code data synchronizing clock	33	UKEY0	Ι	Body key input 0
6	ICVSS	-	Connected to VSS	34	UTAPE0	Ι	Tape switch 0
7	UX2	-	Main system clock	35	UTAPE1	ı	Tape switch 1
8	UX1	Τ	Main system clock	36	AVDD	•	AD converter power supply
9	VDD	-	Power supply	37	UAVREF	ı	AD converter reference voltage
10	UXT1	I	Sub system clock	38	UBUP	—	Backup power supply decision
11	UXT2	-	Sub system clock	39	UFTUNER	0	Function tuner
12	URESET	- 1	Reset	40	VSS	ı	GND
13	UREM	ı	Remote control	41	UMPX	—	FM stereo detection
14	URDSCK	-	Non connect	42	UPERIOD	0	Tuner PLL strobe
15	UJOG1	- 1	Jog encoder 1	43	UJOGB	—	JOG encoder 2
16	UBEAT2	0	Main clock selection 2	44	UBASS	0	BASS control
17	UBEAT1	0	Main clock selection 1	45	UTRE	0	TRE control
18	U+BCTL	0	Switched 5V control	46	UVOL	0	VOL.control
19	UXRESET	0	CD LSI reset	47	USBASS	0	AHB on/off
20	UMCLK	0	CD LSI command clock	48	USMUTE	0	System muting
21	UMDATA	0	CD LSI command data	49	UPOUT	0	Power ON/OFF
22	UMLD	0	CD LSI command load	50	UFCD	0	Function CD
23	UPBMUTE	0	Tape playback mute	51~54	COM0~3	0	LCD common
24	ULATCH	0	Tape IC strobe	55	BIAS	ı	LCD bias voltage
25	UREEL	Ι	Tape end detection	56~58	VLC0~2	-	LCD bias voltage
26	UFAUX	0	Function AUX	59	VSS	•	GND
27	UAVSS	-	AD converter GND	60~99	S0~39	0	LCD segment
28	USAFEYCD	1	CD abnormal voltage detection	100	USTATUS	Ι	LCD LSI status

■ TA2057N (IC1): FM / AM IF AMP & Detector

1.Block d iagrams

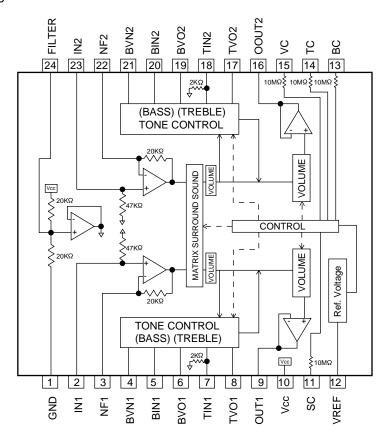


2.Pin function

Pin No.	I/O	Symbol	Function	Pin No.	I/O	Symbol	Function
1	I	AM RF	AMRF signal input	13	0	Lch OUT	Output Lch
2		AM S.SENS		14	0	VCO	Voltage controlled terminal
3		FM S.SENS		15	0	LPF2	When voltage of terminal is MONO
4	-	AM OSC	AM local oscillation circuit				at "H" and ST at "L"
5	0	AM OSC OUT	AM local oscillation signal output	16	0	LPF1	When voltage of terminal is AM at
6	-	VCC	Power supply				"H" and FM at "L"
7	_	AGC	AGC voltage input terminal	17	ı	MPX IN	Multi plex signal input
8	-	GND	Connect to GND	18	0	AM OUT	AM detection signal output
9	0	IF OUT	IF REQ signal output to IC2	19	0	FM OUT	FM detection signal output
10	0	TU IND	Indicator drive output when tuning	20	I	FM QUAD	Bypass to FMIF
11	0	ST IND	Stereo indicator output	21	ı	AM IF IN	Input of AMIF signal
			"H"mono . "L"stereo	22	-	Vst	Fixed voltage output terminal
12	0	Rch OUT	Output Rch	23	0	AM MIX OUT	Output terminal for AM mixer
				24	I	FM IF IN	Input of FMIF signal

■ BH3852S (IC38) : E.Volume

1. Block diagrams

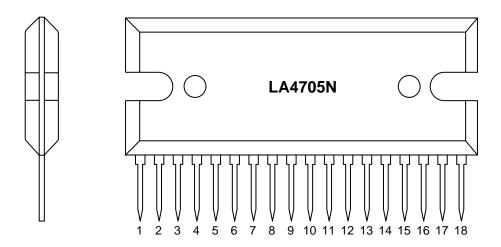


2. Pin function

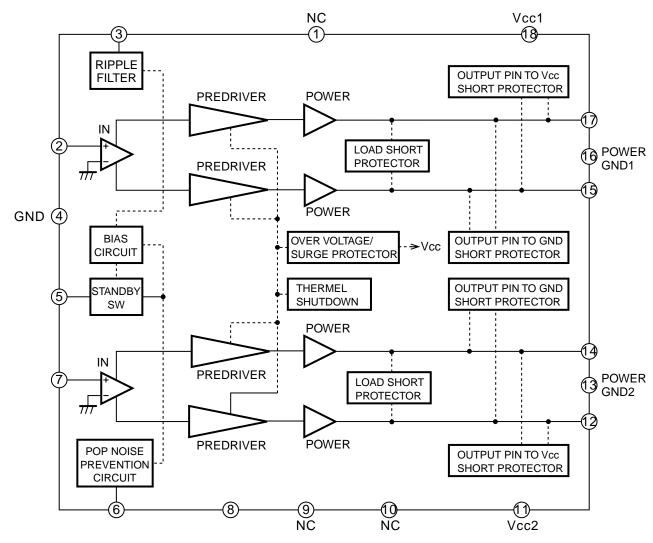
Pin No.	Symbol	I/O	Function	Pin No.	Symbol	I/O	Function
1	GND	-	Connect to GND.	13	BASS	Ι	Terminal for bass control.
2	IN1	ı	Terminal for 1ch volume input.	14	TRE	ı	Terminal for treble control.
3	NF1	_	Terminal for gain adjustment of	15	VOL	1	Terminal for volume control.
			input step AMP.	16	OUT2	0	Terminal for 2ch volume output.
4~6	BASS1	-	Terminal for connection of 1ch	17.18	TRE2	-	Terminal for connection of 2ch
			low-frequency filter.				high-frequency filter.
7.8	TRE1	-	Terminal for connection of 1ch	19~21	BASS2	-	Terminal for connection of 2ch
			high-frequency filter.				low-frequency filter.
9	OUT1	0	Terminal for 1ch volume output.	22	NF2	ı	Terminal for gain adjustment of
10	VCC	-	Terminal for power supply.				input step AMP.
11	LIVE	-	Terminal for surround control.	23	IN2	ı	Terminal for 2ch volume input.
12	VREF	0	Terminal for reference voltage	24	VSET	-	Terminal for filter.
			output.				

■LA4705NA (IC31): 2ch BTL power IC



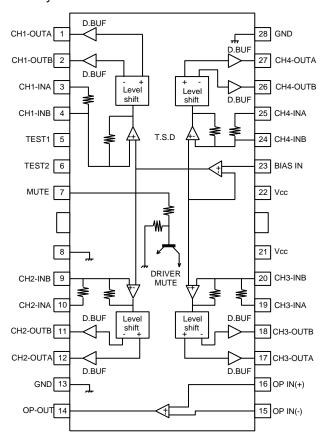


2. Block diagram

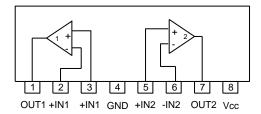


■ BA6897FP (IC602): 4channel driver

1.Pin layout

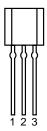


■ BA15218N (IC32 / IC35) : Dual Ope. Amp.



■ KIA78S06P (IC703) : Regulator

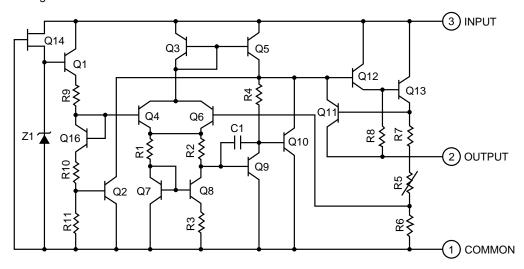
1.Pin layout



2. Pin function

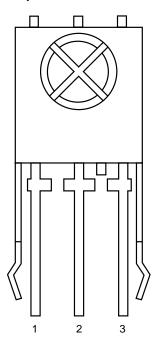
Pin No.	Function			
1	Output			
2	Common			
3	Input			

3.Block diagram



■ PIC-21043SP (IC702) : Remotocontrol receiver

1.Pin layout



2. Pin function

Pin No.	Function
1	Vout
2	Vcc
3	Gnd

<<MEMO>>



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